

[MEMORY ARCHITECTURE AND METHOD FOR REPAIRING A SERIAL ACCESS MEMORY]

Abstract

A memory architecture and method for repairing a serial access memory is disclosed. The memory architecture has a main memory unit and a redundant memory unit made from an independent memory module. The memory architecture further incorporates other circuits including a data selection unit, a fuse box, a comparable logic unit and a pointer control unit. The comparable logic unit uses a first-in-first-out scheme to access serial data to simplify the circuit. Thus, the invention has lower production costs and greater design flexibility.